



PATENT APPLICATION  
Attorney Docket No.: 9898-172  
Client Ref. No.: SS-15671-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Young-Sik KIM, et al.

Serial No.: 09/773,874 Examiner: Mason, Donna K.

Filing Date: January 31, 2001 Group Art Unit: 2111

Confirmation No.: 7968,

Title: SYSTEM ON A CHIP HAVING SYSTEM BUS, EXTERNAL BUS, AND  
BUS ARBITER WITH PROGRAMMABLE PRIORITIES FOR BOTH  
BUSES, SOFTWARE, AND METHOD FOR ASSIGNING  
PROGRAMMABLE PRIORITIES

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

under 37 CFR 1.97 (b); or

(Within three months of filing national application; or date of entry of International application; or before mailing date of first office action on the merits; or before the mailing of a first Office Action after the filing of a request for continued examination under §1.114; whichever occurs last)

Applicant(s) submit herewith Form PTO 1449-Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,  
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450  
Date: June 8, 2005

Li Mei Vermilya

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**INFORMATION DISCLOSURE CITATION  
FORM PTO-1449 (Modified)****U.S. PATENT DOCUMENTS**

<u>Exam Init</u>	<u>Ref</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub Class</u>
_____	_____	5,528,767	Jun. 18, '96	Chen		
_____	_____	5,691,745	Nov. 25, '97	Mital		
_____	_____	6,125,412	Sep. 26, '00	Picard, et al.		

**FOREIGN PATENT DOCUMENTS**

<u>Exam Init</u>	<u>Ref</u>	<u>Document Number</u>	<u>Publication Date</u>	<u>Country</u>	<u>Name</u>
_____	_____	EP0479702	Apr. 8, '92	Europe	Boldt, et al.
_____	_____	EP0518504	Dec. 16, '92	Europe	Fuoco, et al.
_____	_____	EP0564116	Oct. 6, '93	Europe	Drerup, et al.
_____	_____	EP0592213	Apr. 13, '94	Europe	Chester A. Pawlowski
_____	_____	FR2674361	Sep. 25, '92	France	Chasserieau, et al.
_____	_____	WO96/41325	Dec. 19, '96	PCT	Chee, et al.
_____	_____	WO97/06523	Feb. 20, '97	PCT	G.R. Mohan Rao
_____	_____	WO97/26604	Jul. 24, '97	PCT	Fu-Chieh Hsu

**OTHER DOCUMENTS**

<u>Exam Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, etc.</u>
_____	_____	English language abstract of French Publication No. FR2674361
_____	_____	Bursky, D., "486 CPU Packs Peripherals for Real-Time System Needs," Electronic Design, vol. 43, no. 18, 5 Sep. 1995, pp. 167-168
_____	_____	Wingard, D., et al., "Integration Architecture for System-On-A-Chip Design," Custom Integrated Circuits Conference, IEEE, 11 May 1998, pp. 85-88

Examiner: \_\_\_\_\_

Date Considered: \_\_\_\_\_